.................... setup\_timer\_2(T2\_DIV\_BY\_4, 199, 8);

0AC1E: MOVLW 38

0AC20: IORLW 05

0AC22: MOVWF FBA T2CON (1:8 prescale, postcale=4, ON)

0AC24: MOVLW C7

0AC26: MOVWF FBB PR2

.................... setup\_ccp1(CCP\_PWM\_H\_H | CCP\_PULSE\_STEERING\_A | CCP\_PULSE\_STEERING\_SYNC);

0AC06: BCF F8B.2 LATC, clear bit 2

0AC08: MOVLW 0C

0AC0A: MOVWF FBD 0C -> CCP1CON (single output PWM, DC1B=00, all active high)

0AC0C: CLRF FB7 00 -> PWM1CON (pwm delay and auto-shutdown features not used)

0AC0E: CLRF FB6 ECCP1AS, auto-shutdown disabled

0AC10: MOVLW 01

0AC12: MOVWF FB9 PSTR1CON, P1A active ONLY

0AC14: MOVLW FC

0AC16: MOVLB F

0AC18: ANDWF x49,F

0AC1A: MOVLW 00

0AC1C: IORWF x49,F re CCPTMRS0 [defaults TMR2 goes with CCP1 for PWM modes]

.................... setup\_timer\_2(T2\_DIV\_BY\_4, 199, 8);

0ABAA: MOVLW 38

0ABAC: IORLW 05

0ABAE: MOVWF FBA T2CON

0ABB0: MOVLW C7

0ABB2: MOVWF FBB

.................... setup\_ccp1(CCP\_USE\_TIMER1\_AND\_TIMER2 | CCP\_PWM\_H\_H | CCP\_PULSE\_STEERING\_A,0x80);

0ABB4: BCF F8B.2

0ABB6: MOVLW 0C

0ABB8: MOVWF FBD

0ABBA: MOVLW 80

0ABBC: MOVWF FB7

0ABBE: CLRF FB6

0ABC0: BTFSS FBA.2 T2CON

0ABC2: GOTO ABCE

0ABC6: BCF F9E.1 PIR1.1 = TMR2IF

0ABC8: BTFSS F9E.1

0ABCA: GOTO ABC8

0ABCE: BCF F8B.2 LATC

0ABD0: MOVLW 01

0ABD2: MOVWF FB9

0ABD4: MOVLW FC

0ABD6: MOVLB F

0ABD8: ANDWF x49,F

0ABDA: MOVLW 00

0ABDC: IORWF x49,F

0ABDE: MOVLB 0